

REMARKS

Claims 1-3, 5-15, 17-18 and 38-45 are pending in the application. The undersigned thanks the Examiner for her time and courtesy in conducting a telephonic conference with Applicant's attorneys on January 30, 2002. After discussing the amended claims in light of the references cited by the Examiner, the Examiner agreed that the claims as amended herein would distinguish over the art of record. The Examiner further stated that if no more pertinent art is found in an updated search, the amended claims would be allowable.

Attached hereto is a marked-up version of the changes made to the claims by the current amendment. The attached page is captioned "Version with Markings to Show Changes Made".

All of the claims remaining in the application are now clearly allowable. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,  
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Enclosures:

Fax Cover Sheet  
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VERSION WITH MARKINGS TO SHOW CHANGES MADE

In the Claims:

Please amend claims 1, 5-7, 9-11, 17-18, 38-40, and 42 as follows:

1. (Twice Amended) A semiconductor device package, comprising:

a semiconductor die having a first surface on which an integrated circuit and at least one electrically conductive bond pad are fabricated, the die having first and second pairs of opposed lateral edges;

at least one electrically conductive external terminal;

an interposer having a die attach surface and an external surface opposite of the die attach surface disposed in between the semiconductor die and the at least one external terminal, the interposer having at least one electrically conductive interconnect electrically coupling the at least one bond pad of the semiconductor die positioned adjacent to the die attach surface to the at least external terminal positioned adjacent to the external surface; and

a plurality of [pieces] strips of compliant adhesive film extending substantially the entire distance between the first pair of opposed lateral edges disposed in between the semiconductor die and the interposer to adhere the semiconductor die to the die attach surface of the interposer, the strips of compliant adhesive film each extending substantially less than the distance between the second pair of opposed lateral edges.

5. (Twice Amended) The package of claim 1 wherein each of the plurality of [pieces] strips of compliant adhesive film comprises:

a first adhesive layer adhered to the die attach surface of the interposer;

a second adhesive layer adhered to the semiconductor die; and

at least one compliant carrier layer disposed in between the first and second adhesive layers and to which the first and second adhesive layers are adhered.

6. (Twice Amended) The package of claim 1 wherein each of the plurality of [pieces] strips of compliant adhesive film comprises a single layer of elastomer material.

7. (Twice Amended) The package of claim 1 wherein the first surface of the semiconductor die is adhered to the die attach surface of the interposer by the plurality of [pieces] strips of compliant adhesive film.

9. (Twice Amended) The package of claim 1 wherein the plurality of [pieces] strips of compliant adhesive film comprise strips of compliant adhesive film positioned in parallel along a longitude of the semiconductor die.

10. (Twice Amended) The package of claim 1 wherein a first and a second of the plurality of [pieces] strips of compliant adhesive film are positioned at a right angle with respect to each other.

11. (Twice Amended) A device package assembly for a semiconductor die being constructed from a process comprising:

laminating a plurality of [pieces] strips of compliant adhesive film to an interposer having at least one electrically conductive interconnect, the interposer further having a die attach surface to which the semiconductor die is attached, and an external surface opposite of the die attach surface;

attaching to the interposer the semiconductor die having a first surface on which an integrated circuit and at least one electrically conductive bond pad are fabricated, the die having first and second pairs of lateral edges, the strips of compliant adhesive film extending substantially the entire distance between the first pair of opposed lateral edges, the strips each extending substantially less than the distance between the second pair of opposed lateral edges; and

bonding the at least one electrically conductive interconnect to the at least one electrically conductive bond pad.

17. (Twice Amended) The package assembly of claim 11 wherein each of the plurality of [pieces] strips of compliant adhesive film comprises a single layer of elastomer material.

18. (Twice Amended) The package assembly of claim 11 wherein the plurality of [pieces] strips of compliant adhesive film comprise strips of film positioned in parallel along a longitude of the semiconductor die.

38. (Amended) A semiconductor device package, comprising:

a semiconductor die having a first surface on which an integrated circuit and at least one electrically conductive bond pad are fabricated, the die having first and second pairs of lateral edges;

an interposer having a die attach surface and at least one electrically conductive interconnect electrically coupled to at least one bond pad of the semiconductor die; and

a plurality of [pieces] strips of compliant adhesive film extending substantially the entire distance between the first pair of opposed lateral edges [interposed] between the die attach surface and the semiconductor die to adhere the semiconductor die to the die attach surface of the interposer, the strips of compliant adhesive film each extending substantially less than the distance between the second pair of opposed lateral edges.

39. (Amended) The semiconductor device package of claim 38 wherein the [pieces] strips of compliant adhesive film comprise an elastomer material.

40. (Amended) The semiconductor device package of claim 38 wherein the [pieces] strips of compliant adhesive film comprise strips of compliant adhesive material positioned in parallel with a longitude of the semiconductor die.

42. (Twice Amended) A semiconductor device package, comprising:

a semiconductor die having a first surface on which at least one electrically conductive bond pad is fabricated, the die having first and second pairs of lateral edges;

an interposer having a die attach surface and at least one electrically conductive interconnect electrically coupled to at least one bond pad of the semiconductor die; and

a plurality of strips of compliant adhesive film extending substantially the entire distance between the first pair of opposed lateral edges [interposed] between the die attach surface and the semiconductor die to adhere the semiconductor die to the die attach surface of the interposer, the strips of compliant adhesive film each extending substantially less than the distance between the second pair of opposed lateral edges.